

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re PATENT application of )  
HISASHI OHTANI et al )  
Serial No. 09/837,552 ) Group Art Unit: Unassigned  
Filed: 04/19/2001 ) Examiner: Unassigned  
For: SEMICONDUCTOR DEVICE AND )  
MANUFACTURING METHOD )  
THEREOF )

INFORMATION DISCLOSURE STATEMENT

Honorable Assistant Commissioner of Patents


Washington, D.C. 20231

Sir:

In accordance with the provisions of 37 C.F.R. 1.56 and 37 C.F.R. 1.97-1.99, Applicant submits herewith attached Form PTO-1449 listing references known to Applicant and requests that these references be made of record in the above identified application. Copies of the references listed are submitted herewith in accordance with 37 C.F.R. 1.98(a).

The Commissioner is hereby authorized to charge fees under 37 C.F.R. §§1.16, 1.17, 1.20(a), 1.20(b), 1.20(c), and 1.20(d) (except the Issue Fee) which may be required now or hereafter, or credit any overpayment to Deposit Account No. 19-2380. A duplicate copy of this sheet is attached.

Respectfully submitted,

  
Eric J. Robinson  
Reg. No. 38,285  
Nixon Peabody LLP  
8180 Greensboro Drive, Suite 800  
McLean, Virginia 22102  
(703) 790-9110

EJR/amd